

### **IN THE SPECIFICATION**

Please insert the following new paragraph, which includes the following table of identifiers immediately after the subheading “BEST MODE FOR CARRYING OUT THE INVENTION” and before par. [0059]:

<u>1</u>	<u>data processing system</u>
<u>2</u>	<u>architecture library</u>
<u>4</u>	<u>program</u>
<u>5</u>	<u>RAM</u>
<u>6</u>	<u>RISC processor</u>
<u>7</u>	<u>interrupt detecting unit (IU)</u>
<u>8</u>	<u>clock generator</u>
<u>9</u>	<u>data input/output interface</u>
<u>10</u>	<u>logic circuit region (RC region, reconfigurable region or reconfigurable hardware)</u>
<u>11</u>	<u>rapid loading control unit (RLC)</u>
<u>12</u>	<u>rapid logic communication master (RTM)</u>
<u>13</u>	<u>load unit (LU)</u>
<u>14</u>	<u>mapping unit (MU)</u>
<u>15</u>	<u>boundary information memory</u>
<u>16</u>	<u>initial setting function</u>
<u>18</u>	<u>interface circuit</u>
<u>19</u>	<u>object, or divided, circuit</u>
<u>20</u>	<u>architecture codes</u>
<u>21</u>	<u>hardware circuit information</u>
<u>22</u>	<u>software information</u>
<u>23</u>	<u>divided (object) circuit information</u>
<u>24</u>	<u>interface circuit information</u>
<u>25</u>	<u>identification information for identifying the architecture code 20</u>
<u>26</u>	<u>boundary condition</u>
<u>27</u>	<u>other information (such as information on a priority order with respect to other divided circuits, exception processing conditions, dynamic tradeoff conditions, and an execution order for the divided circuits)</u>
<u>28</u>	<u>architectural codes</u>
<u>29</u>	<u>architectural codes</u>
<u>31</u>	<u>step</u>
<u>32</u>	<u>step</u>
<u>33</u>	<u>step</u>
<u>34</u>	<u>step</u>
<u>35</u>	<u>step</u>
<u>41</u>	<u>step</u>
<u>42</u>	<u>step</u>
<u>43</u>	<u>step</u>
<u>44</u>	<u>step</u>
<u>45</u>	<u>step</u>
<u>46</u>	<u>step</u>
<u>47</u>	<u>step</u>

51	circuit block (rxe_plane)
52	wires
53	logic element
54	buses of "layer 1"
55	buses "layer 2"
61	input routes (circuits)
62	output routes (circuits)
63	input interface
63s	selector
64	output interface
64s	selector
65	operation core
66	selector
67	decoder
68	register
69a	selector
69b	selector
70	robot
71	function A controls and carries out data processing for hearing (automated control mechanism)
72	function B controls and carries out data processing for sight (automated control mechanism)
73	function C controls and carries out data processing for speech (automated control mechanism)ability
74	function D controls and carries out data processing for physical functions(automated control mechanism)
75	communication, or functional, unit
80	terminal (that includes data processing system)
81	input/output mechanism
82	sensor (for detecting light, temperature, or the like)
85	mechanism